

## Organizing Committee

### **Patron**

**Prof. S.N. Singh**

Hon'ble Vice Chancellor, M.M.M.U.T.

### **Chairman**

**Dr .S.K. Soni**

Professor & Head, ECED, M.M.M.U.T.

### **Convenor**

**Maj. G.S. Tripathi**

Associate Professor, ECED, M.M.M.U.T.

### **Coordinator(s)**

**Mr. Anupam Sahu**

Assistant Professor, ECED, M.M.M.U.T.

**Mr. G.D. Bharti**

Assistant Professor, ECED, M.M.M.U.T.

### **Co-coordinator**

**Dr. (Mrs.) Rasika N. Dhavse**

Associate Professor, S.V.N.I.T.

### **Committee Members**

**Dr. R. K. Chauhan**, Professor

**Dr. Brijesh Kumar**, Associate Professor

**Dr. Manish Kumar**, Associate Professor

**Dr. Rajan Mishra**, Assistant Professor

**Dr. Dharmendra Kumar**, Assistant Professor

**Dr. Sudhanshu Verma**, Assistant Professor

**Dr. Pooja Lohia**, Assistant Professor

**Dr. B. P. Pandey**, Assistant Professor

### **Contact Details**

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## **About University**

Madan Mohan Malaviya University of Technology, Gorakhpur has been established in year 2013 by Govt. of Uttar Pradesh in the form of a non-affiliating teaching and research university after reconstituting the Madan Mohan Malaviya Engineering College, Gorakhpur which was established in 1962.

Fifty-Seven batches of students have entered its portals to emerge after 4 years of rigorous education under the tutelage of some of the most venerable Teachers, Engineers ready to face the world and create new world. The University is located in the Gorakhpur - Deoria road about 9 Km away from Gorakhpur Railway Station. In addition to UG in Civil Engineering, Chemical Engineering, Computer Science & Engineering, Mechanical Engineering, Electrical Engineering and Electronics & Communication Engineering, University also offers MCA, MBA., B.B.A M.Sc, M. Tech and Ph.D. programme in various specializations.



## **Madan Mohan Malaviya University of Technology Gorakhpur-273010, (U.P)**

Established by U.P. Government Act no-22 of 2013

### **Department of Electronics & Communication Engineering Organizing**

### **Short Term Course on**

## **Recent Advances in Devices, Circuits & Communication (RADCC-2020)**

(Sponsored by TEQIP III)

**Jan 3-7, 2020**

**In association with**



**Department of Electronics Engineering  
Sardar Vallabhbhai National Institute of  
Technology, Surat**

## Theme of Short-Term Course

The objective of this one-week Short Term Course (STC) is to generate the platform for bringing out the challenges in the field of communication and devices circuitry. The recent communication systems are required to meet the competing requirements of enhanced functionality, low loss, reduced size & weight, low cost for transmission and radiation of electromagnetic waves. It is important to notice that the several design objectives in modern efficient and miniaturized systems are self-conflicting.

In view of above stated requirements, the focus of this STC has been designed with the objective of imparting retrospective and prospective of device simulation with emphasis on the low-loss electronic devices, antennas, channel models, 5G communication systems with the following aims:

- To aware the researchers and research minded people about the recent advancement in devices, circuits and communication area.
- To train the faculty and scholars to build more appropriate model in the field of microwave, wireless channel models and its Monte-Carlo simulation.
- To learn different tools for circuit and device modelling in appealing way.

**Areas to be covered:** Lectures and Invited talk on 5G Communication, VLSI circuits and its device application and extensive hands on training on MATLAB, Wireless Insite, Mentor Graphics, Cadence.

The resource persons for the STC shall be the faculty of the Institute itself, eminent speakers from other IIT's / IIIT's / NIT's along with persons from the Industries and academia.

Applications are invited from interested researchers (Ph.D/M.Tech) and faculty members from academia, R&D laboratories, and industries for the participation in the prescribed registration form as indicated in this leaflet.

### Registration and Fee particulars:

Faculties/Researchers from other institutes/Industry persons	<b>Rs 1000/-</b>
Ph.D Scholars/Contractual faculties from parent institute	<b>Rs 500/-</b>

\* Registration Fees are exempted for regular faculty from parent institute.

### Payment Mode:

Participants can pay their registration fees using NEFT in the following account detail:

**Name of Bank :** SBI (MMMEC), Gorakhpur

**A/c No :** 33542824744

**IFSC Code :** SBIN0002578

### How to apply:

A filled in application form in the prescribed format duly signed by appropriate authority along with payment slip should reach to the coordinator(s) by speed post. Alternatively, you can also mail the scanned registration form (including payment details) to following email id latest by **23<sup>th</sup> Dec, 2019**.

[asece@mmmut.ac.in](mailto:asece@mmmut.ac.in)

[dbece@mmmut.ac.in](mailto:dbece@mmmut.ac.in)

### Important Dates:

**Last date for registration :** Dec 18, 2019

**Intimation of selection :** Dec 24, 2019

**Duration of event :** Jan 3-7, 2020

## Registration Form

### Short Term Course

on

### Recent Advances in Devices, Circuits & Communication (RADCC-2020)

(Sponsored by TEQIP III)

**Jan 3-7, 2020**

- 1- Name \_\_\_\_\_  
(In Block Letters)
- 2- Organization \_\_\_\_\_
- 3- Highest Qualification \_\_\_\_\_
- 4- Date of Birth \_\_\_\_\_ Sex \_\_\_\_\_
- 5- Mailing Address \_\_\_\_\_  
\_\_\_\_\_  
E-mail \_\_\_\_\_  
Phone \_\_\_\_\_
- 6- Payment mode: DD/Online
- 7- Accommodation Required Yes/No

### Declaration:

The above information is true to the best of my knowledge. If selected, I agree to abide the by rules and regulations of STC and shall attend the course for entire duration. I also take the responsibility to inform the coordinator(s) in case, I am unable to attend the programme.

Date:

Place:

Signature of Applicant

Head of the Institution  
(Signature & Seal)