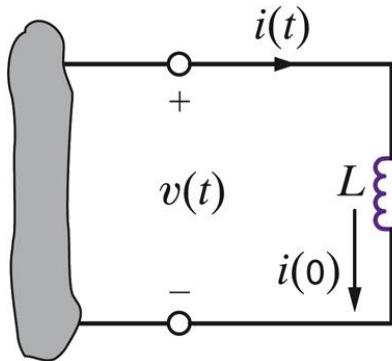
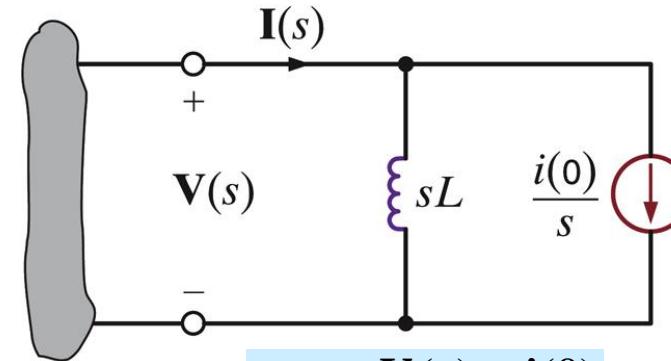
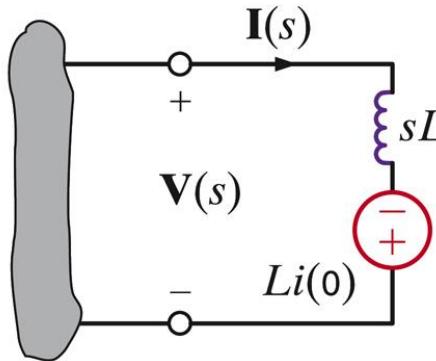


Inductor Models



$$v(t) = L \frac{di}{dt}(t) \Rightarrow V(s) = L(sI(s) - i(0))$$



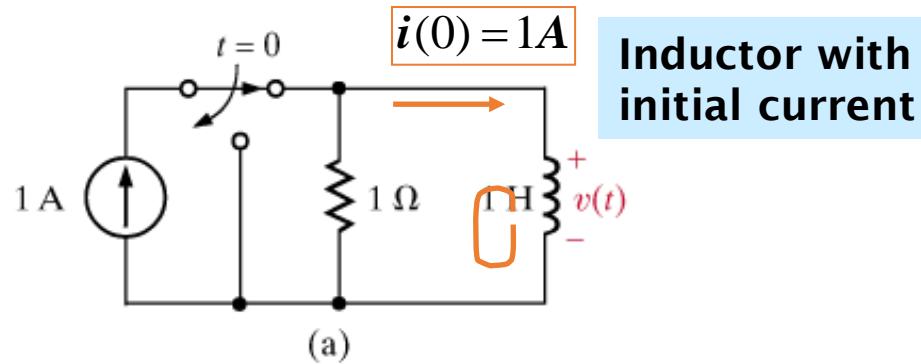
$$I(s) = \frac{V(s)}{s} + \frac{i(0)}{s}$$

$$\mathcal{L}\left[\frac{di}{dt}\right] = sI(s) - i(0)$$

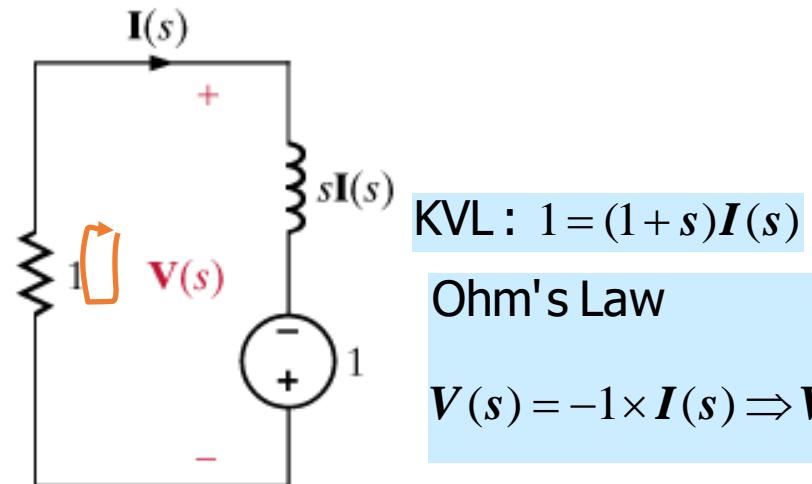
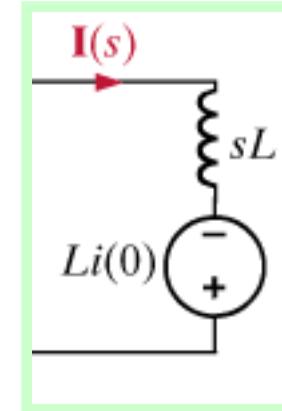
LEARNING BY DOING

Determine the model in the s-domain and the expression for the voltage across the inductor

Steady state for $t < 0$



Inductor with initial current



$$\text{KVL: } 1 = (1 + s)I(s)$$

Ohm's Law

$$V(s) = -1 \times I(s) \Rightarrow V(s) = -\frac{1}{s+1}$$

Equivalent circuit in s-domain

ANALYSIS TECHNIQUES

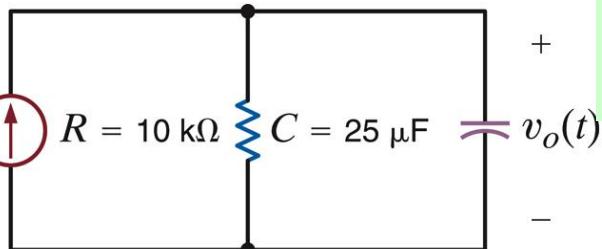
All the analysis techniques are applicable in the s-domain

LEARNING EXAMPLE

Draw the s-domain equivalent and find the voltage in both s-domain and time domain

$$I_S(s) = \frac{3}{s+1}$$

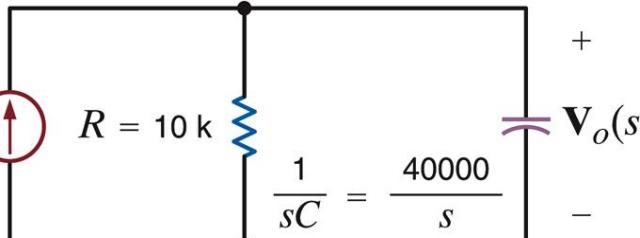
$$i_S(t) = 3e^{-t}u(t) \text{ mA}$$



$$i_S(t) = 0, t < 0 \Rightarrow v_o(0) = 0$$

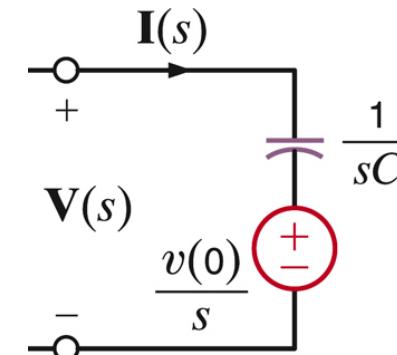
One needs to determine the initial voltage across the capacitor

$$I_S(s) = \frac{3}{s+1}$$



$$V_o(s) = \left(R \parallel \frac{1}{Cs} \right) I_S(s)$$

$$V_o(s) = \frac{\frac{R}{Cs}}{R + \frac{1}{Cs}} I_S(s) = \frac{1/C}{s+1/RC} \times \frac{3 \times 10^{-3}}{s+1}$$



$$V_o(s) = \frac{120}{(s+4)(s+1)} = \frac{K_1}{s+4} + \frac{K_2}{s+1}$$

$$K_1 = (s+4)V_o(s)|_{s=-4} = -40$$

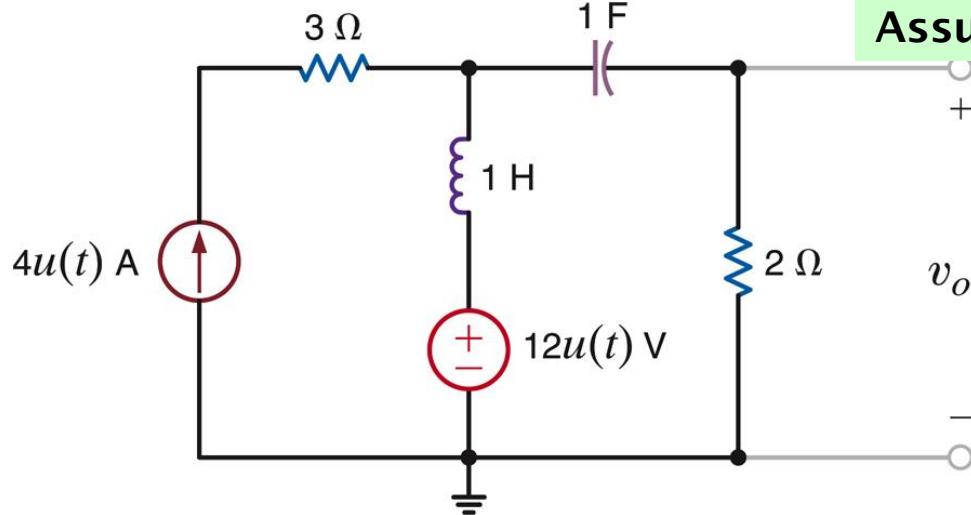
$$K_2 = (s+1)V_o(s)|_{s=-1} = 40$$

$$v_o(t) = 40[e^{-t} - e^{-4t}]u(t)$$



LEARNING EXAMPLE

Find $v_o(t)$ using node analysis, loop analysis, superposition, source transformation, Thevenin's and Norton's theorem.



Node Analysis

Assume all initial conditions are zero

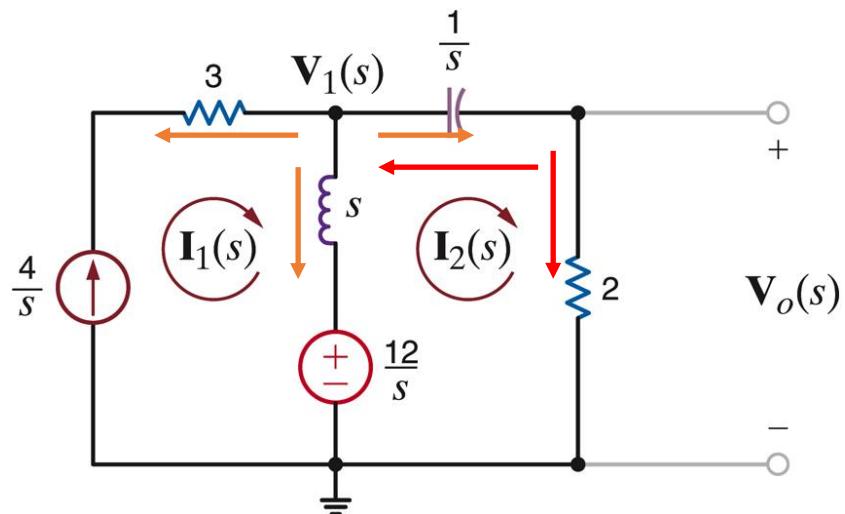
KCL @ V_1

$$v_o(t) - \frac{4}{s} + \frac{V_1(s) - \frac{12}{s}}{s} + \frac{V_1(s) - V_o(s)}{\frac{1}{s}} = 0 \times s$$

KCL@ V_o

$$\frac{V_o(s)}{2} + \frac{V_o(s) - V_1(s)}{\frac{1}{s}} = 0 \times 2$$

Could have used voltage divider here

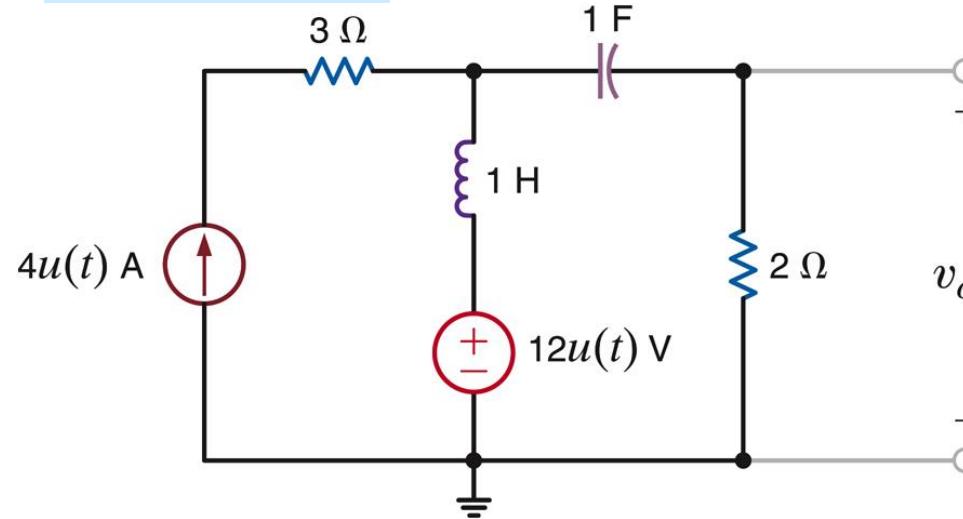


$$(1+s^2)V_1(s) - s^2V_o(s) = \frac{4s+12}{s} \times 2s$$

$$-2sV_1(s) + (1+2s)V_o(s) = 0 \times (1+s^2)$$

$$V_o(s) = \frac{8(s+3)}{(1+s)^2}$$

Loop Analysis



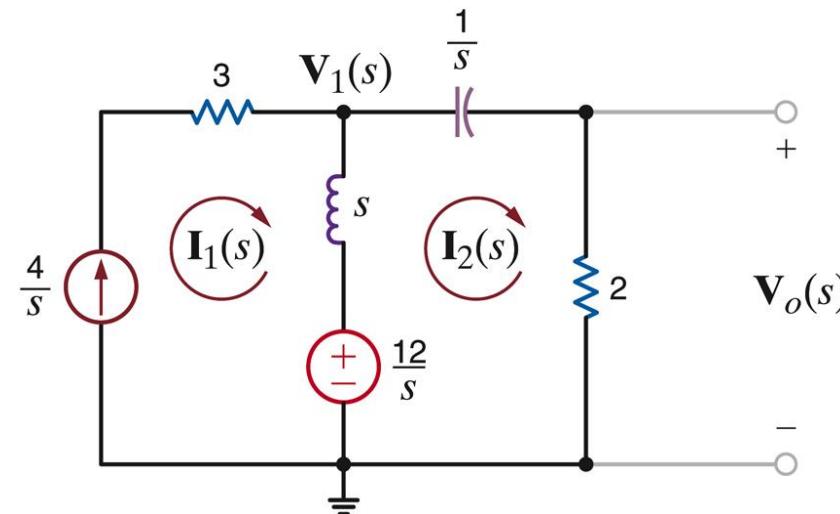
Loop 1

$$I_1(s) = \frac{4}{s}$$

Loop 2

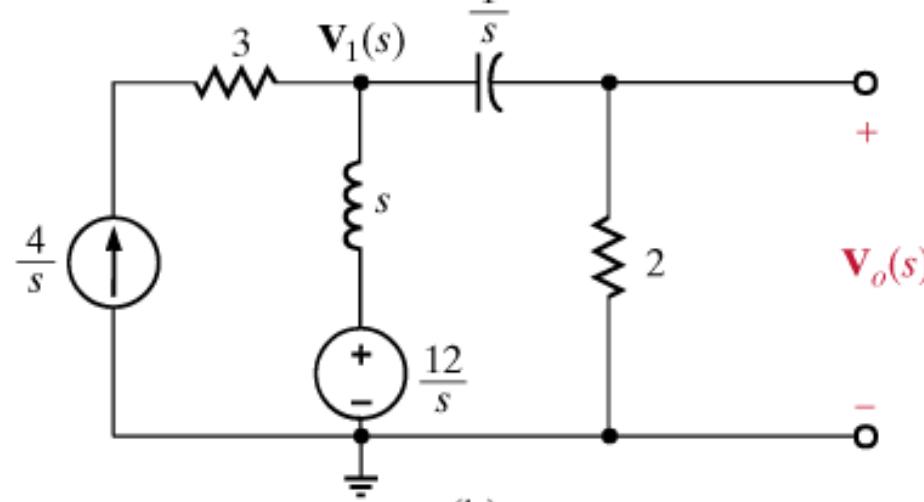
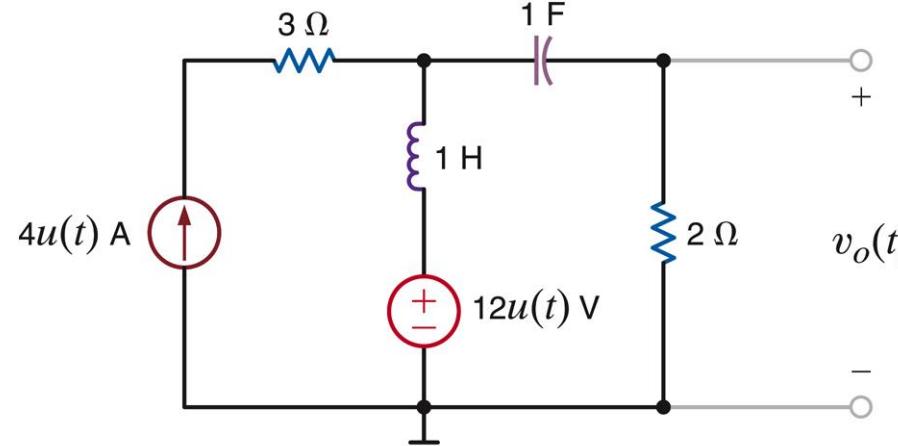
$$v_o(t) s(I_2(s) - I_1(s)) + \frac{1}{s} I_2(s) + 2I_2(s) = \frac{12}{s}$$

$$I_2(s) = \frac{4(s+3)}{(s+1)^2}$$



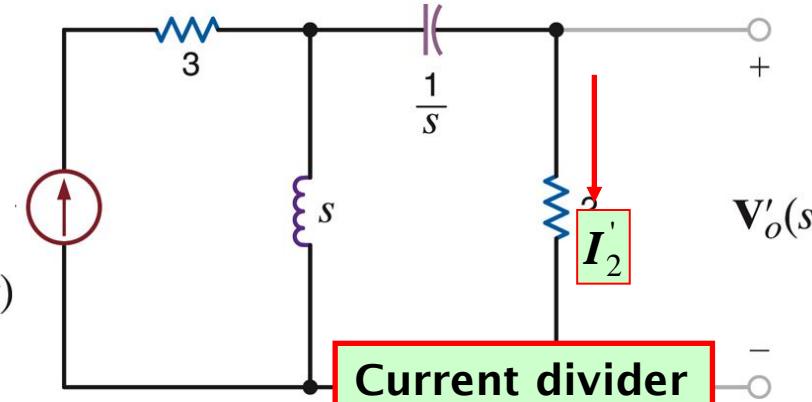
$$V_o(s) = 2I_2(s) = \frac{8(s+3)}{(s+1)^2}$$

Source Superposition



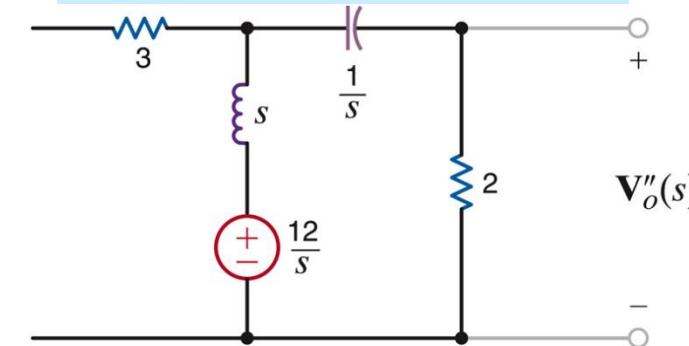
$$V_o(s) = V'_o(s) + V''_o(s) = \frac{8(s+3)}{(s+1)^2}$$

Applying current source



$$V'_o(s) = 2 \times \frac{s}{2 + \frac{1}{s} + s} \times \frac{4}{s}$$

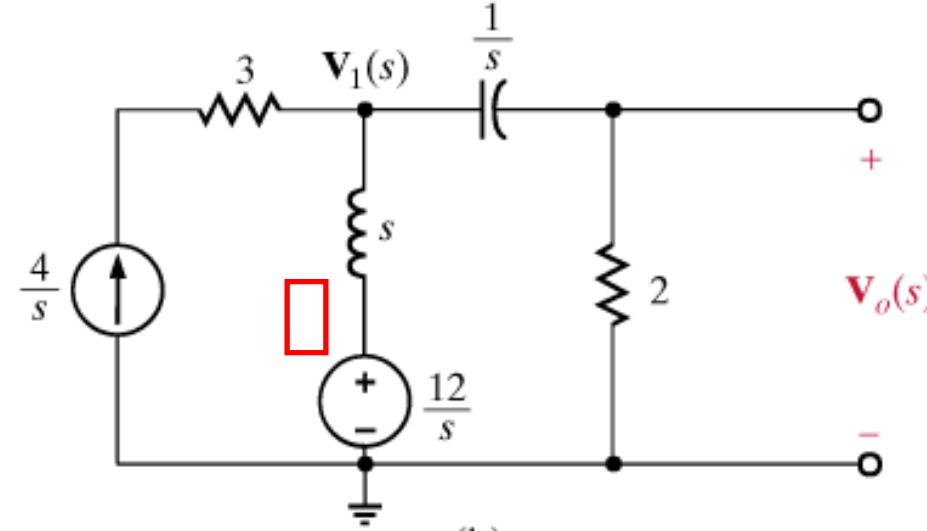
Applying voltage source



Voltage divider

$$V''_o(s) = \frac{2}{2 + \frac{1}{s} + s} \times \frac{12}{s}$$

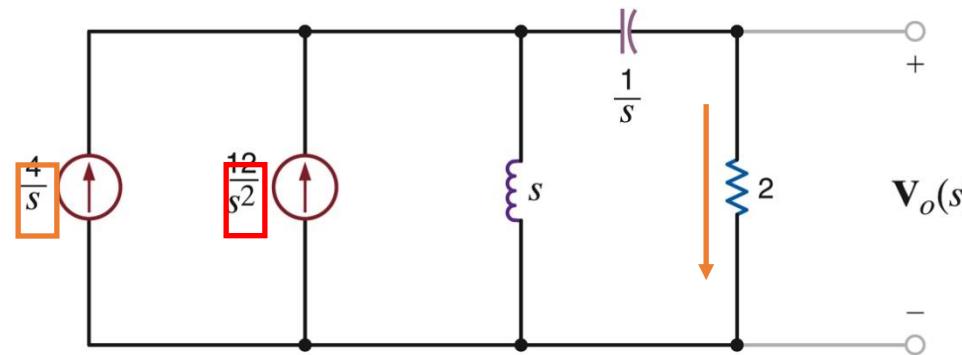
Source Transformation



Combine the sources and use current divider

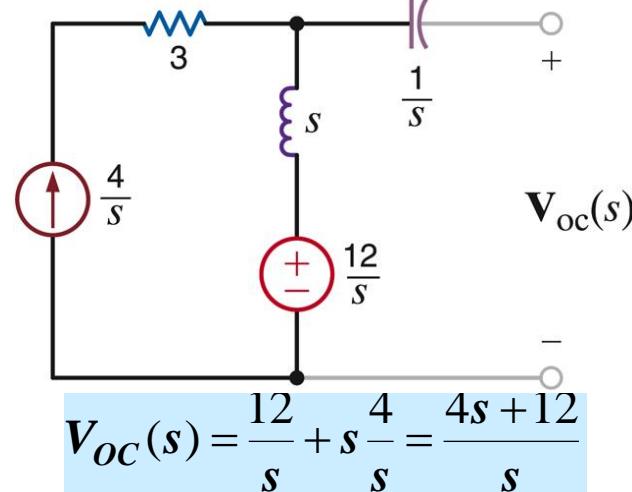
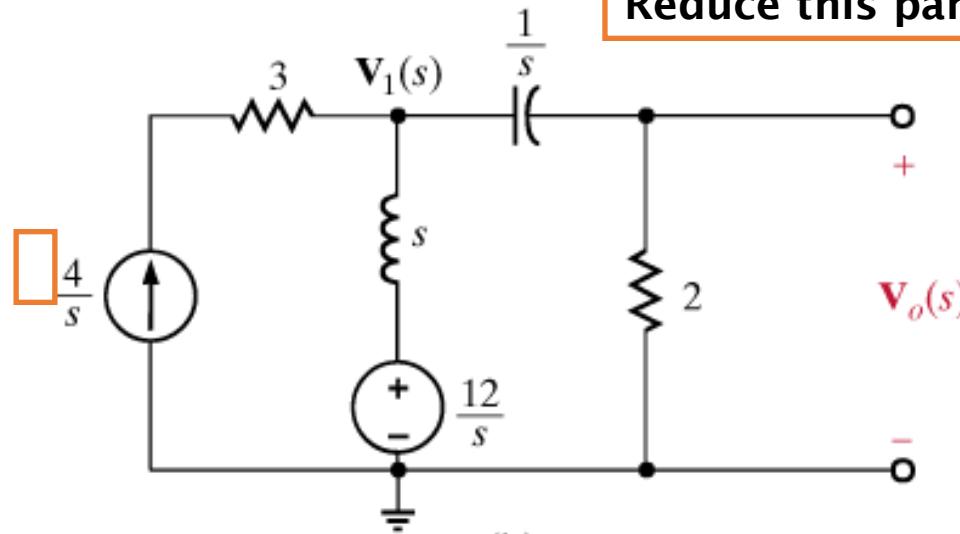
$$V_o(s) = 2 \times \frac{s}{s + \frac{1}{s} + 2} \left(\frac{4}{s} + \frac{12}{s^2} \right)$$

$$V_o(s) = \frac{8(s+3)}{(s+1)^2}$$



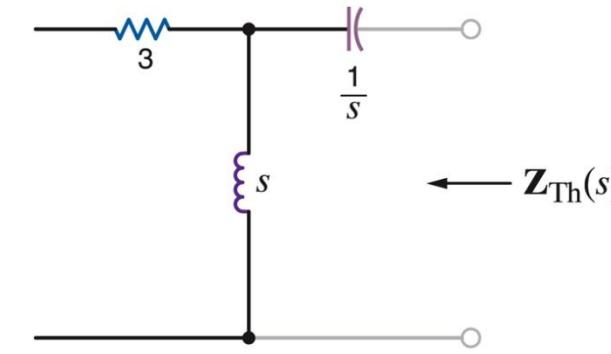
The resistance is redundant

Using Thevenin's Theorem

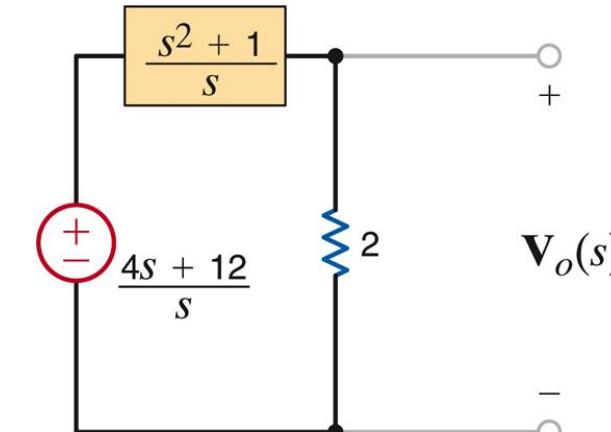


Only independent sources

Reduce this part



$$Z_{th} = \frac{1}{s} + s = \frac{s^2 + 1}{s}$$



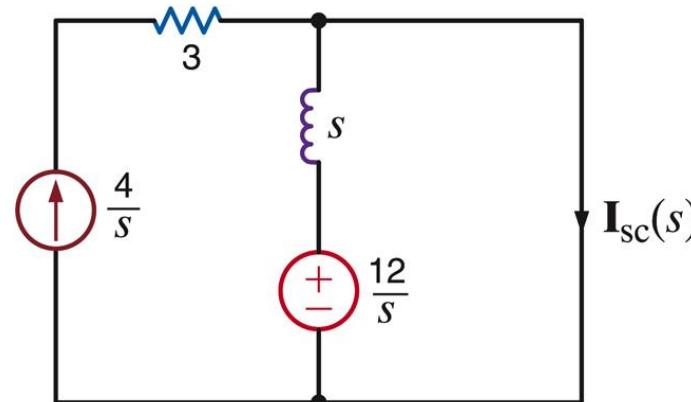
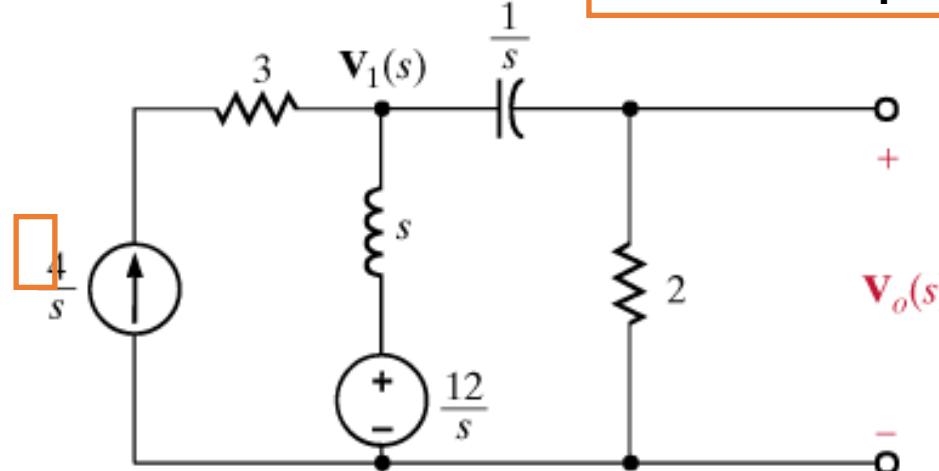
$$V_o(s) = \frac{2}{2 + \frac{s^2 + 1}{s}} \cdot \frac{4s + 12}{s}$$

$$V_o(s) = \frac{8(s+3)}{(s+1)^2}$$

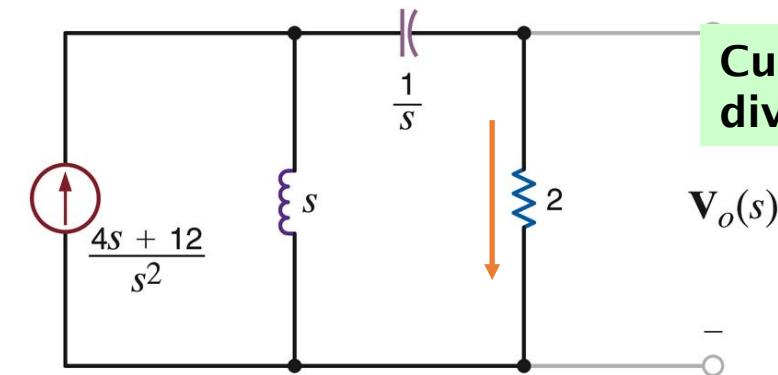
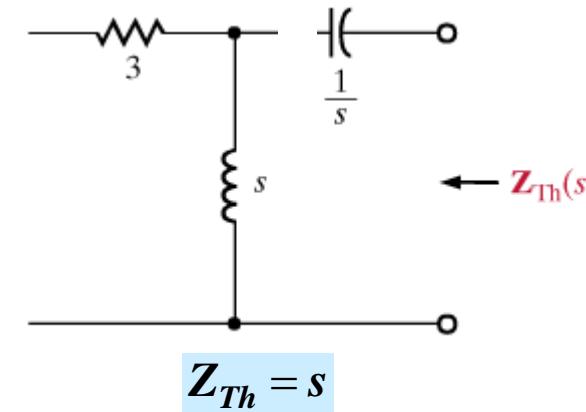
Voltage
divider

Using Norton's Theorem

Reduce this part



$$I_{sc}(s) = \frac{4}{s} + \frac{12/s}{s} = \frac{4s+12}{s^2}$$

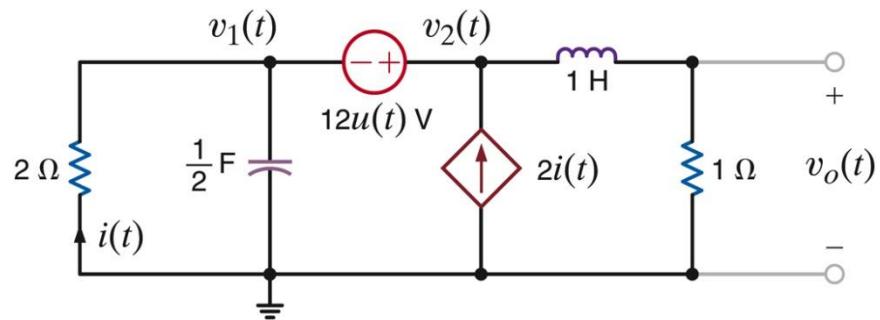


$$V_o(s) = 2 \times \frac{s}{s + \frac{1}{s} + 2} \frac{4s+12}{s^2}$$

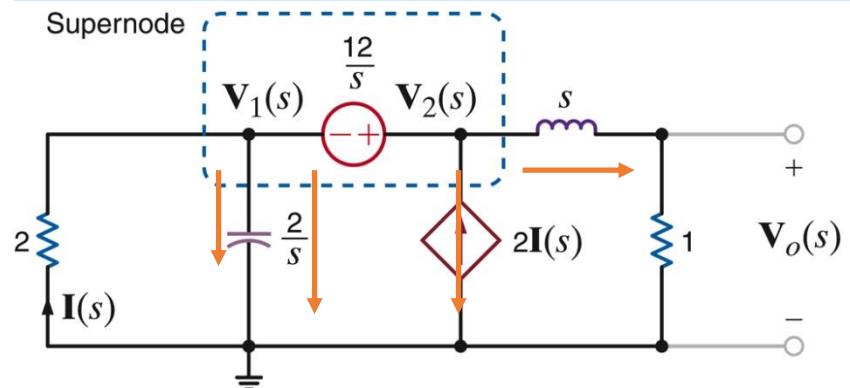
$$V_o(s) = \frac{8(s+3)}{(s+1)^2}$$

LEARNING EXAMPLE

Determine the voltage $v_o(t)$. Assume all initial conditions to be zero



Transforming the circuit to s-domain



$$\text{Supernode constraint: } V_2(s) - V_1(s) = \frac{12}{s}$$

$$\text{KCL@ supernode: } \frac{V_1(s)}{2} + \frac{V_1(s)}{2/s} - 2I(s) + \frac{V_2(s)}{s+1} = 0$$

$$\text{Controlling variable: } I(s) = -\frac{V_1(s)}{2}$$

$$\text{Voltage divider: } V_o(s) = \frac{1}{s+1} V_2(s)$$

Selecting the analysis technique:

- Three loops, three non-reference nodes
- One voltage source between non-reference nodes - supernode
- One current source. One loop current known or supermesh
- If v_2 is known, v_o can be obtained with a voltage divider

$$\text{Doing the algebra: } V_1(s) = V_2(s) - 12/s$$

$$I(s) = -V_2(s)/2 + 6/s$$

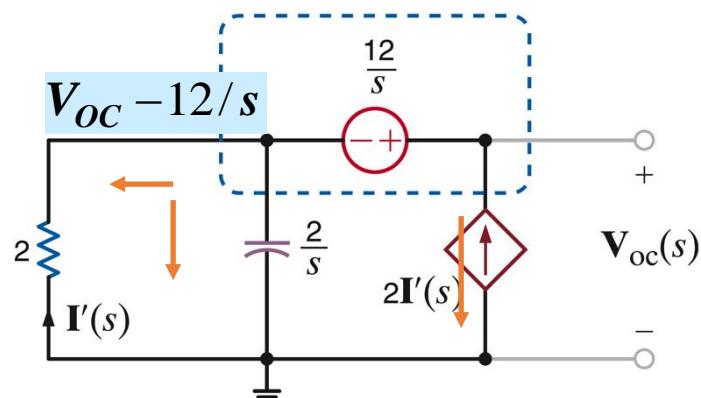
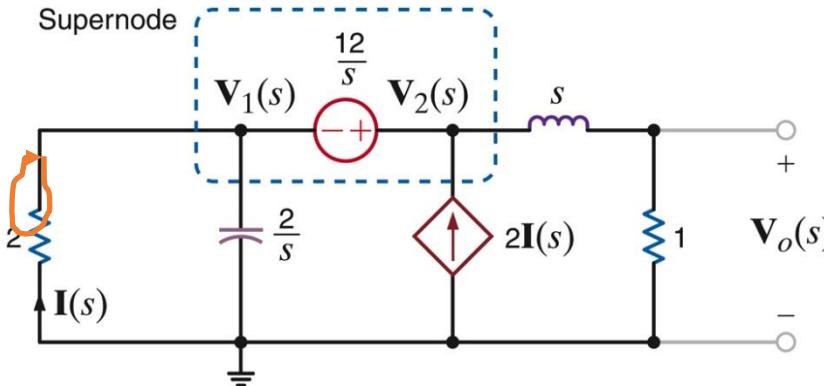
$$(1/2)(s+1)(V_2(s) - 12/s) - 2(-V_2(s)/2 + 6/s) + V_2(s)/(s+1) = 0$$

$$V_2(s) = \frac{12(s+1)(s+3)}{s(s^2 + 4s + 5)}$$

$$V_o(s) = \frac{12(s+3)}{s(s^2 + 4s + 5)}$$

Continued ...

Compute $V_o(s)$ using Thevenin's theorem



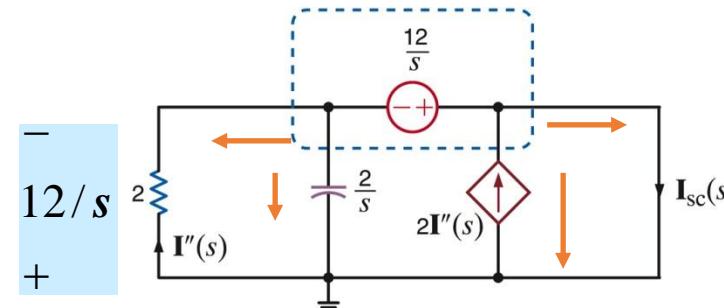
$$\frac{V_{oc} - 12/s}{2} + \frac{V_{oc} - 12/s}{2/s} - 2I' = 0$$

$$I' = -\frac{V_{oc} - 12/s}{2}$$

$$V_{oc}(s) = \frac{12}{s}$$

$I' = 0$

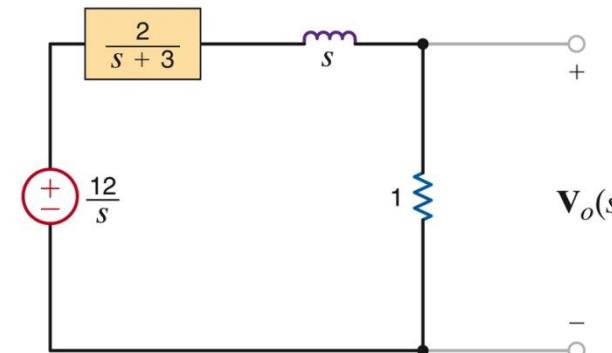
- keep dependent source and controlling variable in the same sub-circuit
- Make sub-circuit to be reduced as simple as possible
- Try to leave a simple voltage divider after reduction to Thevenin equivalent



$$I_{sc} - 2I'' - I'' - 2I''/(2/s) = 0 \quad I'' = 6/s$$

$$I_{sc} = \frac{6(s+3)}{s}$$

$$Z_{TH} = \frac{V_{oc}(s)}{I_{sc}(s)} = \frac{2}{s+3}$$



$$V_o(s) = \frac{1}{1+s+\frac{2}{s+3}} \times \frac{12}{s}$$