# ELECTRONIC MEASUREMENT & INSTRUMENTATION (BEC-29)



#### Instructor Dr. Brijesh Mishra Assistant Professor

Department of Electronics and Communication Engineering Madan Mohan Malaviya University of Technology , Gorakhpur

# UNIT-III Digital to Analog Converters (DAC)

#### What is a DAC?

• A digital to analog converter (DAC) converts a digital signal to an analog voltage or current output.



#### What is a DAC?



# Types of DACs

- Many types of DACs available.
- Usually switches, resistors, and op-amps used to implement conversion
- Two Types:
  - Binary Weighted Resistor
  - R-2R Ladder

- Utilizes a summing op-amp circuit
- Weighted resistors are used to distinguish each bit from the most significant to the least significant
- Transistors are used to switch between V<sub>ref</sub> and ground (bit high or low)

- Assume Ideal Op-amp
- No current into op-amp
- Virtual ground at inverting input
- $V_{\rm out} = -IR_{\rm f}$





If 
$$R_{\rm f} = R/2$$
  
 $V_{\rm out} = -IR_{\rm f} = -\left(\frac{V_1}{2} + \frac{V_2}{4} + \frac{V_3}{8} + \cdots + \frac{V_n}{2^n}\right)$ 

For example, a 4-Bit converter yields

$$V_{\text{out}} = -V_{\text{ref}} \left( b_3 \frac{1}{2} + b_2 \frac{1}{4} + b_1 \frac{1}{8} + b_0 \frac{1}{16} \right)$$

Where  $b_3$  corresponds to Bit-3,  $b_2$  to Bit-2, etc.

- Advantages
  - Simple Construction/Analysis
  - Fast Conversion
- Disadvantages
  - Requires large range of resistors (2000:1 for 12-bit DAC) with necessary high precision for low resistors
  - Requires low switch resistances in transistors
  - Can be expensive. Therefore, usually limited to 8-bit resolution.



Each bit corresponds to a switch:

If the bit is high, the corresponding switch is connected to the inverting input of the op-amp.

If the bit is low, the corresponding switch is connected to ground.







For a 4-Bit R-2R Ladder

$$V_{\text{out}} = -V_{\text{ref}} \left( b_3 \frac{1}{2} + b_2 \frac{1}{4} + b_1 \frac{1}{8} + b_0 \frac{1}{16} \right)$$

For general n-Bit R-2R Ladder or Binary Weighted Resister DAC

$$V_{\text{out}} = -V_{\text{ref}} \sum_{i=1}^{n} b_{n-i} \frac{1}{2^{i}}$$

- Advantages
  - Only two resistor values (R and 2R)
  - Does not require high precision resistors
- Disadvantage
  - Lower conversion speed than binary weighted
     DAC

## Specifications of DACs

- Resolution
- Speed
- Linearity
- Settling Time
- Reference Voltages
- Errors

## Resolution

- Smallest analog increment corresponding to 1 LSB change
- An N-bit resolution can resolve 2<sup>N</sup> distinct analog levels
- Common DAC has a 8-16 bit resolution

Resolution = 
$$V_{LSB} = \frac{V_{ref}}{2^N}$$
  
where  $N = number of bits$ 

## Speed

- Rate of conversion of a single digital input to its analog equivalent
- Conversion rate depends on
  - clock speed of input signal
  - settling time of converter
- When the input changes rapidly, the DAC conversion speed must be high.

## Linearity

• The difference between the desired analog output and the actual output over the full range of expected values



## Linearity

 Ideally, a DAC should produce a linear relationship between the digital input and analog output



## Settling Time

- Time required for the output signal to settle within +/- ½ LSB of its final value after a given change in input scale
- Limited by slew rate of output amplifier
- Ideally, an instantaneous change in analog voltage would occur when a new binary word enters into DAC



## **Reference Voltages**

- Used to determine how each digital input will be assigned to each voltage division
- Types:
  - Non-multiplier DAC: Vref is fixed
  - Multiplier DAC: Vref provided by external source

#### Types of Errors Associated with DACs

- Gain
- Offset
- Full Scale
- Resolution
- Non-Linearity
- Non-Monotonic
- Settling Time and Overshoot

## Gain Error

Occurs when the slope of the actual output deviates from the ideal output



#### Offset Error

 Occurs when there is a constant offset between the actual output and the ideal output



#### Full Scale Error

 Occurs when the actual signal has both gain and offset errors



## **Resolution Error**

- Poor representation of ideal output due to poor resolution
- Size of voltage divisions affect the resolution



## **Non-Linearity Error**

- Occurs when analog output of signal is nonlinear
- Two Types
  - Differential analog step-sizes changes with increasing digital input (measure of largest deviation; between successive bits
  - Integral amount of deviation from a straight line after offset and gain errors removed; on concurrent bits

#### Non-Linearity Error, cont.



#### Non-Monotonic Error

 Occurs when an increase in digital input results in a decrease in the analog output



#### Settling Time and Overshoot Error

- Settling Time time required for the output to fall with in +/- ½ V<sub>LSB</sub>
- Overshoot occurs when analog output overshoots the ideal output



# Applications

- Digital Motor Control
- Computer Printers
- Sound Equipment (e.g. CD/MP3 Players, etc.)
- Electronic Cruise Control
- Digital Thermostat

## References

- Callis, J. B. "The Digital to Analog Converter." 2002. http://courses.washington.edu/jbcallis/lectures/C464\_Lec5\_Sp-02.pdf. 14 March 2006
- "DAC." 2006. http://en.wikipedia.org/wiki/Digital-toanalog\_converter#DAC\_types. 14 March 2006.
- Johns, David and Ken Martin. "Data Converter Fundamentals." © 1997. http://www.eecg.toronto.edu/~kphang/ece1371/chap11\_slides.pdf. 14 March 2006
- Goericke, Fabian, Keunhan Park and Geoffrey Williams. "Digital to Analog Converter." © 2005. http://www.me.gatech.edu/mechatronics\_course/DAC\_F05.ppt. 14 March 2006

#### **THANK YOU**